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Temperature-dependent hysteresis model for Li-ion batteries

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ABSTRACT

The increasing importance of accurate battery state estimations in advanced Battery Management Systems (BMS) underscores the need for precise modelling of battery behaviour and characteristics. While equivalent circuit models are widely utilized for their low computational demands, they face challenges in maintaining precision and adaptability during dynamic conditions, posing a persistent concern for future advancements. This study focuses specifically on the battery hysteresis effect, a complicating factor in the modelling and estimation processes. Open circuit voltage (OCV) measurements and parameter identification for equivalent circuit models were conducted on prevalent Li-ion battery technologies, namely nickel-manganese–cobalt (NMC) and lithium-iron-phosphate (LFP). The experimental results indicate the hysteresis effect becomes more significant with lower temperatures. In this paper, a battery model covering the temperature influence on the hysteresis effect is proposed. The proposed model exhibits an average root mean square error of less than 13 mV. The model holds promise for application in modern battery management systems, offering an enhancement to state-of-charge estimation methodologies.

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Battery modelling; hysteresis effect; Li-ion; temperature dependence

I. Introduction

Battery management systems play a crucial role in ensuring the safe and reliable operation of a battery pack. Nevertheless, advanced BMS systems require an accurate state of charge (SoC) and state of health (SoH) estimation. To meet these demands, modern BMS systems are equipped with battery models that rely on equivalent circuit models (ECM). This choice is driven by the ECM's ability to meet processing power requirements while providing acceptable accuracy.

To model Li-ion battery dynamic behaviour, OCV test and cell impedance measurements to estimate ECM parameters are usually employed [\[1](#page-9-0)[–5\]](#page-9-1). There are several methods for impedance parameter identification. The electrochemical impedance spectroscopy (EIS) test procedure can be used, but it requires specific instrumentation. A hybrid pulse power characterization test can be used as well. However, 30 s of rest might not be sufficient. Therefore, in [\[1–](#page-9-0)[4\]](#page-9-2), longer resting periods were applied in the pulse current test.

An important aspect of battery modelling is OCV. OCV is a function of SoC, and the cell OCV values are determined empirically at numerous SOC points [\[2](#page-9-3)[,6\]](#page-9-4). The OCV test is performed similarly to the pulse current test, which is described in [\[1\]](#page-9-0). For the measurement of the close-to-equilibrium OCV, the length of the resting period is the key aspect, but there is no uniform opinion regarding the resting period length across the literature. In [\[6\]](#page-9-4), authors used a resting time of 30 minutes. In other studies, the resting time was 2 hours [\[1\]](#page-9-0), or 4 hours respectively [\[3\]](#page-9-5). A 24-hour resting period was suggested in [\[7\]](#page-9-6), but the whole test becomes extensively time-consuming. In [\[2\]](#page-9-3), a 20-hour resting period was applied at 50% SoC, but the equilibrium still didn't seem to be reached. However, the error between the measured voltage and the OCV decays the most within the first few hours, so 3–4 hours of resting can be regarded as a good trade-off between accuracy and time-consuming tests.

Temperature dependence on OCV was tested in [\[1\]](#page-9-0) and [\[8\]](#page-9-7). In [\[1\]](#page-9-0), a difference of less than 2 mV was found for OCV at most temperatures. Similarly, in [\[8\]](#page-9-7) it was found that the temperature has a very little impact on OCV parameters. Therefore, it can be assumed that temperature does not have an impact on OCV if a long enough resting period is applied.

There are more ways of the OCV modelling, the most common are a lookup table and description by an analytic function. For a lookup table, the OCV is evaluated by interpolation between table values. In the case of an analytic function, the data can be fitted by a certain function. For example, a polynomial function, exponential function, Gaussian model, or a sum of sinus functions can be used [\[9\]](#page-10-0). The OCV is then calculated by evaluating this function for a specific SoC value. A comparative study in [\[10\]](#page-10-1) showed that the 9th-order

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polynomial model is optimal for OCV modelling for both NMC and LFP cells.

However, the OCV modelling doesn't sufficiently describe the hysteresis effect. Understanding this phenomenon is the key factor in accurate battery modelling. Based on [\[11\]](#page-10-2), the reasons behind the hysteresis effect are entropic effects, mechanical stress, and microscopic distortions within the active electrode materials during lithium insertion or extraction. Another paper [\[12\]](#page-10-3) shows that high current rates create strong inhomogeneities in the particles' lithium contents. Therefore, they induce an ion transfer from lithiated particles towards delithiated particles. These inhomogeneities create the hysteresis effect. The inhomogeneities create different potentials within the particle and electrode material, which creates local SoC differences [\[13\]](#page-10-4). That induces diffusion, which is temperature dependent. Paper [\[13\]](#page-10-4) also shows slower lithium transport within particles at lower temperatures. To quantify and reconstruct the effects under various conditions, new methods must be encountered, considering the load current intensity besides the SOC, SoH, temperature, battery history, and the load current direction as factors influencing the hysteresis [\[12\]](#page-10-3).

In [\[14\]](#page-10-5), authors model the hysteresis effect based on two independent charge and discharge OCV curves. In [\[15\]](#page-10-6), the hysteresis was modelled by a four-state system with 8 different parameters for both charging and discharging. These parameters were identified based on curve-fitting techniques. However, the temperature model for Li-ion battery cells was not included.

In [\[16\]](#page-10-7), the authors use a state-space representation for the real-time battery model and the hysteresis model. The hysteresis model parameters, which affect the hysteresis behaviour are the maximum hysteresis voltage and the convergence rate. The study includes tests at different ambient temperatures and presents temperature-dependent hysteresis parameters for temperatures of 5, 21 °C, and 35 °C. In [\[17\]](#page-10-8), a second-order ECM was used, where ECM parameters were a function of SoC and temperature. However, the hysteresis was modelled by the same method as in [\[16\]](#page-10-7) without considering the temperature effect.

The main contribution of this paper is the proposed battery model, which includes the effect of the temperature on battery hysteresis. That allows more accurate battery modelling and improves SoC estimation in BMS.

The organization of this paper is set as follows. Section II describes the methodology of battery dynamic modelling behaviour, Section III deals with the experimental equipment and experiments, Section IV contains ECM parameters identification results, the hysteresis model is proposed in Section V together with the simulation results, and Section VI concludes the paper.

Figure 1. Equivalent circuit model.

II. Methodology

Modelling Li-ion battery cells using the equivalent circuit model commonly involves conducting the OCV test and impedance test across a broad SoC range. Through these tests, the ECM parameters are characterized as functions of SoC or temperature. In this paper, a second-order ECM is employed, and its representation is illustrated in Figure [1.](#page-2-0)

A. Parameter identification of ECM

The pulse current test was chosen for the ECM parameter identification because it doesn't require any additional equipment. The cell is commonly fully charged before the test starts, and then the cell is discharged by a constant current of 0.5C by steps of 5% SoC (Figure [2\)](#page-3-0). The voltage response is measured for both discharging and charging across the whole SoC range. The voltage response for *n* RC pairs is described by the following equation [\[1\]](#page-9-0):

$$
V_{bat}(t) = V_{OCV} - V_0 - \sum_{k=1}^{n} V_k e^{-\frac{t}{\tau_k}}, \qquad (1)
$$

where V_{OCV} is the open circuit voltage, V_0 is an instantaneous voltage drop across series internal resistance R_0 , τ_k is a time constant of each of parallel RC pairs, and V_k is a voltage drop across each of RC pairs in steady state. In practice, 2 RC pairs are used the most often, so this equation can be rewritten as:

$$
V_{bat}(t) = V_{OCV} - V_0 - V_1 e^{-\frac{t}{\tau_1}} - V_2 e^{-\frac{t}{\tau_2}}, \quad (2)
$$

where τ_1 and τ_2 are shorter and longer time constants. Parameters V_1 , V_2 , τ_1 , and τ_2 can be extracted by curve fitting as shown in Figure [3.](#page-3-1) Curve Fitting Toolbox in MATLAB was used to extract these parameters. The equivalent series resistance is calculated from instantaneous voltage drop and applied current [\[2\]](#page-9-3):

$$
R_0 = \frac{V_0}{I},\tag{3}
$$

$$
R_1 = \frac{V_1}{I},\tag{4}
$$

$$
C_1 = \frac{\tau_1}{R_1},\tag{5}
$$

Figure 2. Pulse current test.

Figure 3. Voltage response on a discharging current pulse.

$$
R_2 = \frac{V_2}{I},\tag{6}
$$

$$
C_2 = \frac{\tau_2}{R_2},\tag{7}
$$

where *I* is a current of the applied pulse.

B. OCV Test

The OCV test follows the same procedure as the pulsed current test, with the difference that there were applied resting periods of 3 hours, and the battery current was reduced to 0.2C. The voltage of the cell, measured after this resting period, is regarded as the OCV. This test is conducted on three cells of the same sort for both battery technologies used in this study to ensure higher accuracy. Subsequently, the OCV is modelled using a lookup table.

III. Experimental setup and experiments

All tests were conducted on Arbin battery tester, model LBT22043 (Figure [4\)](#page-3-2). Every one of the 16 channels provide a current load of 10 A. The constant testing temperature was ensured by Memmert environmental test chamber CTC256 with an adjustable temperature range from -42 °C to $+190$ °C.

Figure 4. Temperature chamber and battery tester.

Table 1. Parameters of two selected cells.

	INR 35E	JGNE 1800
Capacity [Ah]	3.35	1.8
Nominal voltage [V]	3.6	3.2
Usable energy [Wh]	12.06	5.8
Max discharge current [A]	8	5.4
Max charge current [A]	ς	1.8
Max power [W]	25.6	17.3
Weight [kg]	0.05	0.0415
Specific energy [Wh/kg]	241.2	139.76

There were selected two different Li-ion cells for battery testing and modelling. The first one is a cylindrical cell Samsung INR18650-35E, representing the Nickel Manganese Cobalt (NMC) technology of Li-ion batteries, which is widely used in the automotive industry nowadays. The second one is JGNE18650-1800 mAh representing the Lithium Ferro Phosphate technology (LFP). The LFP cells have a lower energy density compared to the NMC cells but have other advantages like a lower price and are much less likely to suffer from a thermal runaway, or they exhibit longer cycle life [\[18\]](#page-10-9). The parameters of these two cells are stated in Table [1.](#page-3-3) Both cells are cylindrical with the same shape and dimensions, a length of 65 mm and a diameter of 18 mm. All cells used in this study were new and without previous cycling.

A. OCV test results

The Open Circuit Voltage (OCV) test was executed on the chosen cells, revealing a distinct difference in OCV curves during charging and discharging, as depicted in Figure [5.](#page-4-0) The discharging curve is identifiable by a lower voltage, whereas the charging curve exhibits a higher voltage. The NMC cell operates within a much wider voltage range up to 4.2 V, and its voltage changes

Figure 5. OCV for discharging and charging of INR18650-35E (NMC) and JGNE-1800 mAh (LFP) cell.

noticeably with the SoC. In contrast, the OCV characteristics of the LFP cell are flatter, posing a challenge for SoC estimation, but offering a more stable characteristic.

B. Impedance test results

The impedance tests were performed under 5 different temperatures (0, 10, 20, 30, and 40 ˚C) with a sampling period of 1 s. The fitted parameters of the INR18650-35E cell are depicted in Figure [6](#page-4-1) and Figure [7](#page-5-0) for both discharging and charging. The parameters of the JGNE1800 mAh cell are in Figure [8](#page-5-1) and Figure [9.](#page-6-0)

It can be seen the resistance parameters R_0 , R_1 , and R_2 are increasing with lower SoC during discharging. This statement applies to both NMC and LFP battery chemistry. Note these parameters are highly temperature dependent and increase with lower temperature.

The R_0 parameter of the NMC cell increased by 43.9% at temperature 0 ˚C compared to 40 ˚C at 50% SoC, whereas for the LFP cell, it was 159.5%. *R*¹ and *R*² also increase much more dramatically for LFP cells at lower temperatures, so it can be concluded that the power capabilities of LFP cells at lower temperatures are significantly worse compared to NMC cells due to the rise of internal impedance.

IV. ECM parameters identification results

In the previous section, the OCV test and the pulse current test for various temperatures were performed to complete the ECM modelling dynamic behaviour of NMC and LFP battery cells. The model is created in MATLAB/Simulink, where the cell current is the input, and the cell voltage is the output. The SoC is calculated based on the coulomb counting method, where the cell current is integrated over time. Parameters *R*0, *R*1, *R*2, *C*1, *C*2, and OCV are a function of SoC, so their values are set based on lookup tables. The polarity of the cell current determines whether the value set for charging or discharging is selected. The model is set to switch between charging and discharging sets as soon as the current polarity changes. The cell voltage V_{cell} is then

Figure 6. ECM parameters of INR18650-35E cell as a function of SoC (discharging).

Figure 7. ECM parameters of INR18650-35E cell as a function of SoC (charging).

Figure 8. ECM parameters of JGNE1800 mAh cell as a function of SoC (discharging).

calculated as:

$$
V_{cell} = V_{OCV} - V_{dyn}, \t\t(8)
$$

where V_{dyn} is a voltage representing voltage drop across the *R*⁰ resistor and across both RC pairs. The simulation results of the pulse current test at different temperatures without modelling hysteresis are shown in Figure [10](#page-6-1) for the LFP cell and Figure [11](#page-6-2) for the NMC cell. It can be found that measured and simulated results correspond very well at higher temperatures (around 40 ˚C), and the lower the temperature, the greater the deviation between both waveforms.

That applies to both NMC and LFP cells. The difference between measured and simulated results is shown

Figure 9. ECM parameters of JGNE1800 mAh cell as a function of SoC (charging).

Figure 10. Comparison of measured voltage and simulation results without modelling hysteresis at 40, 20, and 0 ˚C for LFP.

in Figure [12](#page-7-0) and Figure [13.](#page-7-1) It is clear from these figures that lower temperatures create larger deviations, and the model suffers from lower accuracy. The diffusion of lithium ions is much slower at lower temperatures, which creates larger inhomogeneities within electrode material, and different potentials across the electrode [\[19\]](#page-10-10). It takes a longer time to get closer to the OCV, and the diffusion gradually slows down as the potential gradients decrease. The diffusion finally stops at a voltage level different from the OCV, which creates the hysteresis effect, because the cell voltage of an unloaded cell is lower than OCV during the discharging process, and higher during charging.

To evaluate the difference between the measured and simulated waveforms, the Root Mean Square Error (RMSE) was used. RMSE is calculated as:

RMSE =
$$
\sqrt{\sum_{i=1}^{n} \frac{(u_{\text{sim}(i)} - u_{\text{mes}(i)})^2}{n}},
$$
 (9)

Figure 11. Comparison of measured voltage and simulation results without modelling hysteresis at 40, 20, and 0 ˚C for NMC.

Figure 12. Deviation between measured and simulated results for NMC battery cell.

Figure 13. Deviation between measured and simulated results for LFP battery cell.

Table 2. Calculated RMSE for different temperatures.

	$T[^{\circ}C]$		INR18650-35E		JGNE-1800 mAh	
		Dis.	Char.	Dis.	Char.	
RMSE IV1	40	0.0165	0.0149	0.0043	0.0105	
	30	0.0198	0.0113	0.0056	0.0119	
	20	0.0264	0.012	0.0156	0.0156	
	10	0.0342	0.0141	0.0342	0.0196	
	0	0.0468	0.023	0.055	0.0164	

where *n* is the number of data points, *usim* is the simulated voltage response, and *umes* is the measured voltage. RMSE was calculated from charging and discharging data for both cells, the results are shown in Table [2,](#page-7-2) and visualized in Figure [14.](#page-7-3) RMSE clearly increases with lower temperatures. However, RMSE is much lower for charging than discharging. That means, the model is more accurate for charging, and the hysteresis effect is more significant during the discharging process.

The calculated RMSE at 20 ˚C for the NMC cell was 26.4 mV, which is relatively high, so the hysteresis effect can not be ignored for the battery modelling, especially if the operating temperatures are lower than the room temperature. Therefore, more advanced modelling approaches are needed for a better description of Li-ion battery dynamic behaviour.

Figure 14. The RMSE from temperature for both tested cells.

Table 3. Hysteresis model parameters.

	NMC cell	LFP cell
Q[Ah]	3.35	1.8
η		
γ	0.01	0.01
a_d	0.0461	0.0563
b_d	-0.0275	-0.0618
M_{ch} [V]	0.0151	0.0148

V. Proposed hysteresis model and simulation

The most commonly used model of battery hysteresis can be found in [\[20,](#page-10-11) [16\]](#page-10-7), or [\[14\]](#page-10-5). The hysteresis effect is described by a discrete-time equation:

$$
h[k+1] = A_H h[k] + (1 - A_H) sgn(i_b) M, \qquad (10)
$$

where *M* is the maximum hysteresis polarization, *sgn* is the sign function, i_b is the battery current, and A_H is represents the expression:

$$
A_H = e^{\left(-\left|\frac{\eta i_b \gamma \Delta t}{Q}\right|\right)},\tag{11}
$$

where η stands for the battery coulombic efficiency, *γ* representing the convergence rate, $Δt$ is a sampling period of 1 s, and *Q* is the battery capacity. These equations don't take into account the temperature effect, which influences the maximum hysteresis polarization *M*. Therefore, the model can be adjusted to express *M* as a function of the temperature and current polarization. The RMSE values in Figure [14](#page-7-3) indicate the hysteresis impact. Therefore, these data can be fitted to express the maximum hysteresis polarization *M*. For the discharging process, these data can be fitted by exponential equation for both cell types:

$$
M_d = a_d e^{(b_d T)}, \t\t(12)
$$

where a_d and b_d are fitted constants, and *T* is the temperature in ˚C. However, for the charging process, the data don't exhibit exponential characteristics, so the maximum hysteresis polarization during the charging process *Mch* was calculated as an average RMSE value across the whole temperature range. All model param-eters can be found in Table [3.](#page-7-4) Fitted results of M_d are shown in Figure [15.](#page-8-0)

Figure 15. The fitted RMSE of discharging from temperature based on the exponential equation.

Figure 16. Comparison of measured voltage, simulation results without the hysteresis model, and simulation results including the hysteresis model at 0 ˚C for NMC cell.

A. The simulation results

The simulation results of the proposed model were compared to the measured waveform, and the calculated RMSE values are shown in Table [4.](#page-8-1) The biggest improvement was observed during discharging at 0 ˚C for the NMC, where the RMSE was reduced by 77.1%. The voltage waveforms of both simulations are shown in Figure [16.](#page-8-2) The simulation results of a battery model including the proposed hysteresis model exhibit much better accuracy compared to the battery model without the hysteresis model. It has also the potential to exhibit better accuracy compared to the traditional hysteresis model since the maximum hysteresis polarization *M* is a function of the current polarity and the temperature. Even higher accuracy could be obtained by incorporating the SoC effect on the battery hysteresis. The average RMSE of the proposed model is 0.0099 V for the NMC cell, and 0.0095 V for the LFP cell.

B. Model verification

In Figure [16,](#page-8-2) the simulation results were compared to the measured results, where the same testing procedure was used for parameter identification tests. However, the ECM parameters may differ for different cycling profiles. Therefore, the model verification was made by applying different discharging C-rates, lengths of the

Table 4. Calculated RMSE for different temperatures including the proposed hysteresis model.

	T I°CI	INR18650-35E		JGNE-1800 mAh	
		Dis.	Char.	Dis.	Char.
RMSE IVI	40	0.0081	0.0101	0.0073	0.0058
	30	0.0111	0.0108	0.0062	0.0045
	20	0.0095	0.0092	0.0058	0.0045
	10	0.0096	0.0073	0.0124	0.0081
	0	0.0107	0.0125	0.0288	0.0119

Figure 17. Comparison of measured voltage and simulation results of the proposed model with a discharging C-rate of 0.2C at 25 ˚C for NMC cell.

Figure 18. Comparison of measured voltage and simulation results of the proposed model with a discharging C-rate of 0.5C at 25 ˚C for NMC cell.

discharging pulse, and temperature. The pulse current test in Figure [2](#page-3-0) was adjusted to use the discharging Crates of 0.2C, 0.5C, and 1C at the ambient temperature of 25 °C. To perform the parameter identification, the length of the discharging pulse was 15 minutes for the C-rate of 0.2C, 6 minutes for 0.5C, and 3 minutes for 1C, so the ECM parameters can be identified with the step of 5% SoC.

The results of these tests were compared to the simulation results of the proposed model in Figure [17](#page-8-3) for 0.2C, Figure [18](#page-8-4) for 0.5C, and Figure [19](#page-9-8) for 1C. All results show a high accuracy with the RMSE of 0.0121, 0.0105, and 0.0151 V respectively. Results with the Crates different from 0.5C show slightly higher RMSE because the internal impedance slightly varies from the C-rate. The dependency of the R_0 parameter from the

Figure 19. Comparison of measured voltage and simulation results of the proposed model with a discharging C-rate of 1C at 25 ˚C for NMC cell.

Figure 20. Parameter *R*⁰ as a function of SoC at different Crates.

C-rate is shown in Figure [20.](#page-9-9) It can be seen, that the higher the C-rate, the lower the R_0 parameter. This might be a consequence of a higher cell core temperature due to higher discharging C-rate and larger losses.

VI. Conclusion

In this paper, the temperature dependency on Li-ion battery hysteresis was studied and an improved hysteresis model was proposed. The OCV and pulse current tests were performed to extract the OCV curve and the internal impedance parameters of NMC and LFP battery chemistries. The created battery models were subjected to a simulation of the pulse current test at different temperatures and compared to the measured waveforms. The simulation results at 40 ˚C show very high accuracy even for the model without hysteresis modelling. However, with lower temperatures, the deviations were much larger, which were visible, especially during the discharging process. The hysteresis effect becomes more significant due to the slower diffusion of lithium ions at lower temperatures. To avoid inaccurate modelling and problems with SoC estimation in BMS, the battery hysteresis model was proposed considering the effect of the temperature. The maximum hysteresis

polarization *M* was proposed as a function of the current polarity and the temperature. The model verification showed the average RMSE of 0.0125 V. Therefore, the proposed model has a great ability to estimate the battery terminal voltage, thus it can improve the SoC estimation in advanced battery management systems.

Disclosure statement

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