

<p>ATMTKA 899</p>	<p>UDK 621.371.56:534.8 IFAC IA 4.7.1:2.1.8 Originalni znanstveni članak</p> <p>AUTOMATIKA 42(1–2),5–12(2001) <b>AKTIVNI SUSTAVI ZA ZAŠTITU OD BUKE U CIJEVIMA</b> <i>Prof. dr. sc. Branko Somek</i> <i>Department of electroacoustics, Faculty of Electrical Engineering and Computing, University of Zagreb</i> <i>Unska 3, HR-10000 Zagreb, Croatia</i> <i>Dr. sc. Martin Dadić</i> <i>Department of electrical engineering fundamentals and measurements, Faculty of Electrical Engineering and Computing, University of Zagreb, Unska 3, HR-10000 Zagreb, Croatia</i> <i>Doc. dr. sc. Mladen Maletić</i> <i>Department of electroacoustic, Faculty of Electrical Engineering and Computing, University of Zagreb</i> <i>Unska 3, HR-10000 Zagreb, Croatia</i></p> <p>Uz dani prikaz razvoja aktivne zaštite od buke, analizirani su elementi sustava aktivne zaštite od buke u cijevima, a na temelju te analize napravljen je model elemenata. Posebno je provedena analiza zvučnika te je napravljen model zvučnika u z-domeni, prikladan za analizu sustava koji imaju analogne i digitalne dijelove. Primjenom takvog modela možemo u vremenskoj domeni analizirati rad i konvergenciju pojedinih adaptivnih algoritama obrade signala. Kao primjer provedena je analiza rada sustava aktivne zaštite na pojednostavljenom modelu ventilacijskog kanala primjenom FXLMS algoritma, te je pokazan jak utjecaj prijenosne karakteristike zvučnika na spektar snage zvučnog signala preostale buke.</p> <p>(Sl. 12, Tabl. 2, Lit. 25 – original na engleskom)</p> <p><i>Autori</i> adaptivni filtri aktivna zaštita od buke elektroakustički prevornici ventilacijski kanali zvučnici</p> <p>ISSN 0005–1144 ATKAAF 42(1–2),5–12(2001)</p>		<p>ATMTKA 900</p> <p>UDK 519.8:575.8 IFAC IA 2.0:1.1.0 Pregledni članak</p> <p>AUTOMATIKA 42(1–2),13–22(2001) <b>EVOLUCIJSKI ALGORITMI (I) – POBUDE I NAČELA</b> <i>Doc. dr. sc. Darko Grundler</i> <i>Sveučilište u Zagrebu, Tehnološki fakultet, Prilaz baruna Filipovića 30, 10000 Zagreb</i> <i>E-mail: darko.grundler@sk.tel.hr; WWW: http://public.srce.hr/~dgrund</i></p> <p>U tekstu je sažeto prikazan povijesni razvoj i osnovna zamisao primjene mehanizma prirodne evolucije pri optimiranju, učenju i modeliranju. Usporedo su objašnjeni glavni pojmovi i postupci prirodne i formalno predočene evolucije poznate pod skupnim imenom evolucijski algoritmi (skraćeno EA). Navedena je podjela inteligentnih algoritama u koje spadaju i EA, te navedene njihove značajke, prednosti i područja primjene. Jednostavnim primjerom ilustrirana je praktična predodžba pojmova i postupci EA.</p> <p>(Sl. 4, Tabl. 1, Lit. 35 – original na hrvatskom)</p> <p><i>Autor</i> evolucija evolucijski algoritmi fenotip genotip izbor jedinka optimiranje populacija primjerenost</p> <p>ISSN 0005–1144 ATKAAF 42(1–2),13–22(2001)</p>
<p>ATMTKA 901</p>	<p>UDK 621.85-181.4 IFAC IA 4.0.6 Originalni znanstveni članak</p> <p>AUTOMATIKA 42(1–2),23–27(2001) <b>KOMPENZACIJA TRENJA U MIKROSUSTAVIMA UPRAVLJANJA NA DALJINU</b> <i>Peter T. Szemes, Peter Korondi</i> <i>Department of Automation, Technical University of Budapest, 1111 Budapest, Budafoki út 8, F.II., Hungary</i> <i>Phone: +36-1-463-1165, Fax: +36-1-463-3163, {szemes, korondi}@elektro.get.bme.hu</i> <i>Hideki Hashimoto</i> <i>Institute of Industrial Science, University of Tokyo, Department of Electrical Engineering</i> <i>7-22-1, Roppongi, Minato Ku Tokyo 106 Japan</i> <i>hashimoto@vss.iis.u-tokyo.ac.jp</i></p> <p>Opisana je izvedba mikrosustava za rad na daljinu koji omogućava bez stresa obavljanje mikroradnji, kao što su montaža i proizvodnja. Prikazano je haptičko sučelje kojim se oponaša dodir uvećanog mikroobjeka prstima rukovatelja. Također je opisan koncept sustava i simulator sustava. Istraživanje izloženo u ovome radu, osim što može uvesti daljinsko upravljanje na mikro razini u mnogim granama industrije, otvara i mogućnosti primjene za štednju resursa, energije i troškova.</p> <p>(Sl. 7, Lit. 8 – original na engleskom)</p> <p><i>Autori</i> upravljanje na daljinu, haptičko sučelje paralelna veza, serijska veza bilateralnost, mikrorad mreža, simulator</p> <p>ISSN 0005–1144 ATKAAF 42(1–2),23–27(2001)</p>		<p>ATMTKA 902</p> <p>UDK 621.313.323 IFAC IA 5.5.4:4.7.1 Originalni znanstveni članak</p> <p>AUTOMATIKA 42(1–2),29–36(2001) <b>VEKTORSKI UPRAVLJANI ELEKTROMOTORNI POGONI S RELUKTANTNIM SINKRONIM MOTORIMA UZ DEFINIRANU DINAMIKU ZATVORENOG KRUGA BRZINE VRTNJE</b> <i>Prof. Ing. Ján Vittek, PhD.; Assoc. Prof. Ing. Juraj Altus, PhD.</i> <i>University of Zilina, Department of Electric Traction and Energetic, 010 26 Zilina, Slovak Republic</i> <i>Prof. Stephen James Dodds, Dr. Roy Perryman</i> <i>University of East London, School of Electrical and Manufacturing Engineering, Dagenham, Essex, RM8 2AS, United Kingdom</i></p> <p>Prikazana je nova koncepcija sustava upravljanja brzinom vrtnje elektromotornog pogona s reluktantnim sinkronim motorima, temeljena na dvijema metodama upravljanja. Konvencionalna vektorska metoda nadograđena je elementom sustava koji predstavlja dinamiku zatvorenog kruga po brzini vrtnje. Budući da je dinamika zatvorenog sustava prikazana proporcionalnim članom prvog reda, čiji pol odbire korisnik, predložena koncepcija može biti šire primjenjiva. Da bi se poboljšala robusnost sustava, dodana je vanjska petlja upravljanja zasnovana na adaptivnom upravljanju s referentnim modelom. Simulacijski rezultati prikazani u radu potvrđuju valjanost predložene koncepcije.</p> <p>(Sl. 6, Lit. 9 – original na engleskom)</p> <p><i>Autori</i> linearizacija adaptivno upravljanje s referentnim modelom nelinearni sustav upravljanja estimator reluktantni sinkroni motor klizni režimi</p> <p>ISSN 0005–1144 ATKAAF 42(1–2),29–36(2001)</p>

<p>ATMTKA 900</p> <p style="text-align: right;">UDK 519.8:575.8 IFAC IA 2.0;1.1.0 Review</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),13-22(2001) <b>EVOLUTIONARY ALGORITHMS (I) – INSPIRATIONS AND PRINCIPLES</b> <i>Doc. dr. sc. Darko Grundler</i> <i>Sveučilište u Zagrebu, Tekstilno tehnološki fakultet, Prilaz baruna Filipovića 30, 10000 Zagreb</i> <i>E-mail: darko.grundler@sk.tel.hr, WWW: http://public.srce.hr/~dgrund</i></p> <p>Text shortly presents the basic idea and historical development of application of a natural evolution mechanism for optimizing, learning and modeling. The main concepts and methods of natural and formal representation of evolution known under the common name »evolution algorithms« (EA for short) are explained side by side. Types of intelligent algorithms including EA are mentioned and its characteristics, advantages and fields of application are given. Simple examples illustrate the practical representation of EA notions and methods. <i>(Fig. 4, Tab. 1, Ref. 35 – original in Croatian)</i></p> <p style="text-align: right;"><i>Author</i></p> <p><i>evolution</i> <i>evolutionary algorithms</i> <i>evolutionary computing</i> <i>fitness</i> <i>genotype</i> <i>individual</i> <i>optimization</i> <i>phenotype</i> <i>population</i> <i>selection</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),13-22(2001)</p>		<p>ATMTKA 899</p> <p style="text-align: right;">UDK 621.371.56:534.8 IFAC IA 4.7.1;2.1.8 Original scientific paper</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),5-12(2001) <b>ACTIVE NOISE CONTROL IN DUCTS</b> <i>Prof. dr. sc. Branko Somek</i> <i>Department of electroacoustics, Faculty of Electrical Engineering and Computing, University of Zagreb</i> <i>Unska 3, HR-10000 Zagreb, Croatia</i> <i>Dr. sc. Martin Dadić</i> <i>Department of electrical engineering fundamentals and measurements, Faculty of Electrical Engineering and Computing, University of Zagreb, Unska 3, HR-10000 Zagreb, Croatia</i> <i>Doc. dr. sc. Mladen Maletić</i> <i>Department of electroacoustic, Faculty of Electrical Engineering and Computing, University of Zagreb</i> <i>Unska 3, HR-10000 Zagreb, Croatia</i></p> <p>After reviewing the development of active noise control principles, we analyzed the elements of the active noise control system in ducts. On the basis of this analysis, we created a model of these elements. Especially, we brought a model of the loudspeaker in the z-domain, suitable for description of systems containing analog and digital parts. Such model enabled us to analyze work and convergence of the adaptive signal processing algorithms applied to active noise control. As an example, we analyzed performance of FXLMS algorithm on simplified model of active noise control system in ventilation duct, and have shown a strong influence of the loudspeaker's transfer function on the power spectrum of the error signal. <i>(Fig. 12, Tab. 2, Ref. 25 – original in English)</i></p> <p style="text-align: right;"><i>Authors</i></p> <p><i>active noise control</i> <i>adaptive filters</i> <i>electroacoustical transducers</i> <i>loudspeakers</i> <i>ventilation ducts</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),5-12(2001)</p>
<p>ATMTKA 902</p> <p style="text-align: right;">UDK 621.313.323 IFAC IA 5.5.4;4.7.1 Original scientific paper</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),29-36(2001) <b>VECTOR-CONTROLLED RELUCTANCE SYNCHRONOUS MOTOR DRIVES WITH PRESCRIBED CLOSED-LOOP SPEED DYNAMICS</b> <i>Prof. Ing. Ján Vittek, PhD.; Assoc. Prof. Ing. Juraj Altus, PhD.</i> <i>University of Zilina, Department of Electric Traction and Energetic, 010 26 Zilina, Slovak Republic</i> <i>Prof. Stephen James Dodds, Dr. Roy Perryman</i> <i>University of East London, School of Electrical and Manufacturing Engineering, Dagenham, Essex, RMS 2AS, United Kingdom</i></p> <p>A new speed control system for electric drives employing reluctance synchronous motors is presented. Design control system combines two control methods. Conventional vector control method is here completed with forced dynamics closed-loop control. Since the closed-loop system response is a first order lag whose pole location can be chosen by the user, the drive may be included as an actuator in a larger scale control scheme to which linear control system design methods can be applied. To improve robustness of the design control structure, the outer control loop based on model reference adaptive control is added. Simulation results presented show good correspondence with theoretical predictions and MRAC outer control loop improves overall drive performances. <i>(Fig. 6, Ref. 9 – original in English)</i></p> <p style="text-align: right;"><i>Authors</i></p> <p><i>linearisation</i> <i>model reference adaptive control</i> <i>non-linear system control</i> <i>observers</i> <i>reluctance synchronous motor</i> <i>sliding-mode control</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),29-36(2001)</p>		<p>ATMTKA 901</p> <p style="text-align: right;">UDK 621.85-181.4 IFAC IA 4.0.6 Original scientific paper</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),23-27(2001) <b>FRICTION COMPENSATION FOR MICRO TELE-OPERATION SYSTEMS</b> <i>Peter T. Szemes, Peter Korondi</i> <i>Department of Automation, Technical University of Budapest, 1111 Budapest, Budafoki út 8, F.II., Hungary</i> <i>Phone: +36-1-463-1165, Fax: +36-1-463-3163, {szemes, korondi}@elektro.get.bme.hu</i> <i>Hideki Hashimoto</i> <i>Institute of Industrial Science, University of Tokyo, Department of Electrical Engineering</i> <i>7-22-1, Roppongi, Minato Ku Tokyo 106 Japan</i> <i>hashimoto@yss.iis.u-tokyo.ac.jp</i></p> <p>In this project, we construct micro tele-operation systems which enable human operators to perform micro tasks, such as assembly or manufacturing, without feeling a stress. We introduce haptic interfaces that give operators the impression as if he/she were touching the expanded micro objects with his/her fingers. We construct simulator systems modeled on remote environment. In this paper we give an outline and concept of this project. This research project can not only extend bilateral tele-operation to many other industries, it can also extend this human-friendly technique and thus help realize savings in resources, energy, costs and human support. <i>(Fig. 7, Ref. 8 – original in English)</i></p> <p style="text-align: right;"><i>Authors</i></p> <p><i>tele-operation, haptic interfaces</i> <i>parallel link, serial link</i> <i>master-slave, bilateral</i> <i>micro works, network</i> <i>simulator, scaling</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF</p>

<p>ATMTKA 903</p>	<p style="text-align: right;">UDK 621.311.68 IFAC IA 5.5.4:4.7.1 Originalni znanstveni članak</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),37-44(2001)</p> <p style="text-align: center;"><b>KASKADNO UPRAVLJANJE PARALELNO SPOJENIM IZMJENJIVAČIMA ZASNOVANO NA KLIZNIM REŽIMIMA</b></p> <p style="text-align: center;"><i>Rafael Ramos, Domingo Biel</i> <i>Dpt. d'Enginyeria Electrónica. E.U.PV.G. UPC. C/ Victor Balaguer s/n. 08800-Vilanova i la Geltrú (Barcelona, SPAIN)</i> <i>e-mail: {lara, biel}@eel.upc.es</i></p> <p style="text-align: center;"><i>Francesc Guinjoan</i> <i>Dpt. d'Enginyeria Electrónica. UPC.Modul C4.Campus Nord. C/Gran Capitán s/n. 08034-Barcelona SPAIN</i> <i>e-mail: guinjoan@eel.upc.es</i></p> <p style="text-align: center;"><i>Enric Fossas</i> <i>Institut d'Organització i Control de Sistemes Industrials, Av. Diagonal, 647, planta 11. ETSEIB. UPC. 08028-Barcelona, SPAIN</i> <i>e-mail: fossas@ioc.upc.es</i></p> <p>U ovom se članku opisuje postupak projektiranja sustava upravljanja modularnim izmjenjivačkim sustavom sačinjenim od <math>N</math> paralelno spojenih izmjenjivača. Sustav upravljanja osigurava regulaciju izlaznog izmjeničnog napona i ravnomjerno strujno opterećenje svih izmjenjivača, što se postiže pomoću skupa kliznih površina i odgovarajućeg upravljanja koje osigurava gibanje sustava po njima. Postupak projektiranja sustava upravljanja dodatno je pojednostavljen postavljanjem skupa ograničenja u vidu ograničenja parametara invertera te ograničenja amplitude i frekvencije izlaznog napona. Funkcionalnost sustava upravljanja ilustrirana je simulacijskim i eksperimentalnim rezultatima dobivenim uz otporno i nelinearno opterećenje izmjenjivačkog sustava. <i>(Sl. 12, Lit. 12 – original na engleskom)</i></p> <p style="text-align: right;"><i>Autori</i> izmjenjivač, kaskadno upravljanje paralelni moduli, klizni režimi</p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),37-44(2001)</p>		<p>ATMTKA 904</p> <p style="text-align: right;">UDK 621.313.333 IFAC IA 5.5.4:4.7.1 Prethodno priopćenje</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),45-51(2001)</p> <p style="text-align: center;"><b>VISOKOFREKVENCIJSKI INTERFERENCIJSKI SIGNALI GENERIRANI U SUSTAVIMA S PWM IZMJENJIVAČIMA, DUGAČKIM KABELIMA I ASINKRONIM MOTORIMA</b></p> <p style="text-align: center;"><i>Prof. Ing. Viktor Valouch, CSc.; Ing. Jiří Škramlík; Prof. Ing. Ivo Doležel, CSc.</i> <i>Institute of Electrical Engineering, Academy of Sciences of the Czech Republic, Dolejškova 5 182 02 Praha 8, Czech Republic</i></p> <p>Pri radu asinkronih izmjeničnih motora napajanih iz statičkih izmjenjivača pojavljuju se visokofrekvencijski interferencijski signali koji su posljedica parazitnih struja između faza i parazitnih struja prema zajedničkoj točki. Ovi interferencijski signali mogu nepoželjno utjecati na rad obližnjih telekomunikacijskih i signalnih kabela i raznih elektroničkih uređaja. Ove se pojave u članku analiziraju teoretskim razmatranjima, koja su potkrijepljena eksperimentalnim rezultatima specijalnih mjerenja smetnji izazvanih parazitnim strujama. Evaluirane su uglavnom frekvencijske karakteristike odabranih dijelova sustava i njihov doprinos ukupnom harmonijskom spektru. <i>(Sl. 15, Lit. 4 – original na engleskom)</i></p> <p style="text-align: right;"><i>Autori</i> struje zajedničkog spoja diferencijalne parazitne struje visokofrekvencijski interferencijski signali asinkroni motori parazitarne pojave</p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),45-51(2001)</p>
<p>ATMTKA 905</p>	<p style="text-align: right;">UDK 621.314.6 IFAC IA 5.5.4:4.7.1 Originalni znanstveni članak</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),53-61(2001)</p> <p style="text-align: center;"><b>KOREKCIJA FAKTORA FAZNOG POMAKA TROFAZNOG TRORAZINSKOG UZLAZNOG ISPRAVLJAČA ZASNOVANOG NA PWM-U</b></p> <p style="text-align: center;"><i>Miro Milanović, Alenka Hren, Franc Mihalic</i> <i>University of Maribor, Faculty of Electrical Engineering and Computer Science, Smetanova 17, SI-2000 Maribor, Slovenia</i> <i>milanovic@uni-mb.si</i></p> <p>U radu je razvijen PWM algoritam za upravljanje matricno-strukturiranim trofaznim trorazinskim ispravljačem. Takav način upravljanja uzet je radi mogućnosti korekcije faktora pomaka (<math>\cos \varphi</math>) na jediničnu vrijednost. Veza između prekidačkih funkcija i zahtjeva na impulsno-širinski modulator jasno opisuje sva ograničenja modulacijskog algoritma. Razvijeni modulacijski algoritam omogućuje korekciju faktora pomaka bez mjerenja ulazne struje pretvarača. Za potrebe te korekcije dovoljno je samo mjerenje faznog kuta između napona i struje na ulazu ispravljača. <i>(Sl. 7, Tabl. 2, Lit. 7 – original na engleskom)</i></p> <p style="text-align: right;"><i>Autori</i> matricni pretvarač ispravljač prekidačka funkcija impulsno-širinska modulacija faktor pomaka – <math>\cos \varphi</math></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),53-61(2001)</p>		<p>ATMTKA 906</p> <p style="text-align: right;">UDK 621.313.322 IFAC IA 5.5.4:4.7.1 Originalni znanstveni članak</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),63-69(2001)</p> <p style="text-align: center;"><b>LIMITERI UZBUDE MALIH SINKRONIH GENERATORA</b></p> <p style="text-align: center;"><i>Goran Erceg, Nikola Tonković, Romina Erceg</i> <i>University of Zagreb, Faculty of Electrical Engineering and Computing</i> <i>Unska 3, 10000 Zagreb, Republic of Croatia</i></p> <p>Mali sinkroni generatori pri radu u elektroenergetskom sustavu slijede promjene napona sustava. Limitiranje uzbuđne struje generatora omogućava rad generatora unutar granica pogonske karte te povećava sigurnost i raspoloživost generatora u sustavu. Ovaj članak sadrži algoritme za određivanje limita maksimalne i minimalne uzbuđne struje te za implementaciju u digitalnom sustavu regulacije napona generatora. Ponašanje limitera uzbuđne struje generatora eksperimentalno je potvrđeno na laboratorijskom modelu. <i>(Sl. 9, Lit. 6 – original na engleskom)</i></p> <p style="text-align: right;"><i>Autori</i> uzbuđni sustav sinkronog generatora limiter maksimalne uzbuđe limiter minimalne uzbuđe</p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),63-69(2001)</p>

<p>ATMTKA 904</p>	<p style="text-align: right;">UDK 621.313.333 IFAC IA 5.5.4:4.7.1 Preliminary communication</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),45-51(2001)</p> <p style="text-align: center;"><b>HIGH-FREQUENCY INTERFERENCES PRODUCED IN SYSTEMS CONSISTING OF PWM INVERTER, LONG CABLE AND INDUCTION MOTOR</b></p> <p style="text-align: center;"><i>Prof. Ing. Viktor Váloach, CSc.; Ing. Jiří Škramlík; Prof. Ing. Ivo Doležel, CSc. Institute of Electrical Engineering, Academy of Sciences of the Czech Republic, Dolejškova 5 182 02 Praha 8, Czech Republic</i></p> <p>The operation of inverter-fed asynchronous motor drives is inherently accompanied by high-frequency interferences caused particularly by parasitic currents of the common and differential modes. These emissions propagated by conduction and radiation may unfavourably affect the operation of nearby telecommunication and signal cables and various low-current devices. The paper deals with an analysis of these phenomena based on theoretical considerations and results of special measurements of the common and differential mode disturbances. Evaluated are mainly the frequency characteristics of selected parts of the system and their contributions to the resultant harmonic spectra.</p> <p><i>(Fig. 15, Ref. 4 – original in English)</i> <span style="float: right;"><i>Authors</i></span></p> <p><i>common mode currents differential mode currents high-frequency interferences induction motor parasitic phenomena</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),45-51(2001)</p>		<p style="text-align: right;">UDK 621.311.68 IFAC IA 5.5.4:4.7.1 Original scientific paper</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),37-44(2001)</p> <p style="text-align: center;"><b>MASTER-SLAVE SLIDING-MODE CONTROL DESIGN IN PARALLEL-CONNECTED INVERTERS</b></p> <p style="text-align: center;"><i>Rafael Ramos, Domingo Biel Dpt. d'Enginyeria Electrónica. E.U.P.V.G. UPC. C/ Víctor Balaguer s/n. 08800-Vilanova i la Geltrú (Barcelona, SPAIN) e-mail: {lara, biel}@eel.upc.es Francesc Guinjoan Dpt. d'Enginyeria Electrónica. UPC.Modul C4.Campus Nord. C/Gran Capitán s/n. 08034-Barcelona SPAIN e-mail: guinjoan@eel.upc.es Enric Fossas Institut d'Organització i Control de Sistemes Industrials, Av. Diagonal, 647, planta 11. ETSEIB. UPC. 08028-Barcelona, SPAIN e-mail: fossas@ioc.upc.es</i></p> <p>This work presents the design of a master-slave sliding-mode control scheme for a modular inverter system composed of <math>N</math> parallel-connected Buck-based single inverters. AC output voltage regulation and balanced current-sharing among the single inverters is achieved by means of a set of switching surfaces and the corresponding sliding control laws. On the other hand, a set of design restrictions is established in terms of inverter parameters and AC output signal amplitude and frequency, this facilitating the subsequent design procedure. Simulation and experimental results for both resistive and nonlinear loads are provided to illustrate the application of the method.</p> <p><i>(Fig. 12, Ref. 12 – original in English)</i> <span style="float: right;"><i>Authors</i></span></p> <p><i>buck inverter, master-slave parallel modules, sliding-mode control</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),37-44(2001)</p>
<p>ATMTKA 906</p>	<p style="text-align: right;">UDK 621.313.322 IFAC IA 5.5.4:4.7.1 Original scientific paper</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),63-69(2001)</p> <p style="text-align: center;"><b>EXCITATION LIMITERS FOR SMALL SYNCHRONOUS GENERATORS</b></p> <p style="text-align: center;"><i>Goran Erceg, Nikola Tonković, Romina Erceg University of Zagreb, Faculty of Electrical Engineering and Computing Unska 3, 10000 Zagreb, Republic of Croatia</i></p> <p>Small synchronous generators connected to electrical power system operate by following changes in the system voltage. Limiting of the generator excitation current enables generator operation within limits of the power chart diagram and increases the safety and availability of generator in the system. This paper includes algorithms for the determination of maximum and minimum excitation current limits and the implementation within digital control system of generator voltage. The performance of generator excitation current limits was experimentally verified on a laboratory model.</p> <p><i>(Fig. 9, Ref. 6 – original in English)</i> <span style="float: right;"><i>Authors</i></span></p> <p><i>synchronous generator excitation system maximum excitation limiters minimum excitation limiters</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),63-69(2001)</p>		<p style="text-align: right;">UDK 621.314.6 IFAC IA 5.5.4:4.7.1 Original scientific paper</p> <p style="text-align: center;">AUTOMATIKA 42(1-2),53-61(2001)</p> <p style="text-align: center;"><b>INPUT DISPLACEMENT FACTOR CORRECTION FOR THREE-PHASE THREE-LEVEL AC TO DC PWM-BASED BOOST RECTIFIER</b></p> <p style="text-align: center;"><i>Miro Milanovic, Alenka Hren, Franc Mihalic University of Maribor, Faculty of Electrical Engineering and Computer Science, Smetanova 17, SI-2000 Maribor, Slovenia milanovic@uni-mb.si</i></p> <p>Pulse width modulation (PWM) strategy for a matrix structured three-phase three-level AC to DC boost rectifier is developed. Such approach has been used on purpose to control the input displacement power factor close to unity. The connection between matrix switching function and PWM requirement very well describes all restrictions that occur in the modulation algorithm. This modulation algorithm enables the input displacement factor correction without an input current sensor. The only necessary control variable is the measured displacement angle between input voltage and input current.</p> <p><i>(Fig. 7, Tab. 2, Ref. 7 – original in English)</i> <span style="float: right;"><i>Authors</i></span></p> <p><i>matrix converter ac-dc converter switching function pulse width modulation input displacement factor</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),53-61(2001)</p>

ATMTKA 907

UDK 621.382.32.022  
IFAC IA 3.2.3  
Originalni znanstveni članak

AUTOMATIKA 42(1-2),71-76(2001)

**ELEKTROTOPLINSKO MODELIRANJE I SIMULACIJA UČINSKOG MOSFET-A**

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Za postizanje što bolje optimizacije sklopova i sustava energetske elektronike danas se zahtijeva elektrotoplinska simulacija učinkovitih poluvodičkih sklopki. Za to su potrebni točni, no ne i presloženi elektrotoplinski modeli učinkovitih poluvodičkih sklopki, pogodni za primjenu u tržišno dostupnim simulatorima sklopova energetske elektronike. U članku je prikazana IsSpice realizacija elektrotoplinskog modela učinkovitog MOSFET-a. Model se sastoji od električnog i toplinskog dijela koji međusobno izmjenjuju vrijednost varijabli. Elektrotoplinski model ispitan je mjerenjem na stvarnom sklopu.

*(Sl. 7, Lit. 10 – original na engleskom)*

*Autori*

*elektrotoplinska simulacija*  
*modeliranje*  
*učinski MOSFET*

ISSN 0005-1144  
ATKAAF 42(1-2),71-76(2001)

			<p>ATMTKA 907 <span style="float: right;">UDK 621.382.32.022 IFAC IA 3.2.3 Original scientific paper</span></p> <p style="text-align: center;">AUTOMATIKA 42(1-2),71-76(2001)  <b>ELECTRO-THERMAL MODELLING AND SIMULATION OF A POWER-MOSFET</b>  <i>Željko Jakopović, Viktor Šunde, Zvonko Benčić</i>  <i>Faculty of Electrical Engineering and Computing, Unska 3, Zagreb, Croatia</i></p> <p>Electro-thermal simulation of power electronic semiconductors is now required in accurate optimisation of power electronic circuits and systems. This requires accurate, but not too complex, electro-thermal models of power semiconductors to be used in commercially available power electronic circuit simulators. Realization of one such electro-thermal model for power MOSFET in IsSpice is described in the paper. Model consists of electrical and thermal part with interactive exchange of variables. Electro-thermal model was tested on real circuit example.</p> <p><i>(Fig. 7, Ref. 10 – original in English)</i> <span style="float: right;"><i>Authors</i></span></p> <p><i>electro-thermal simulation modelling power MOSFET</i></p> <p style="text-align: right;">ISSN 0005-1144 ATKAAF 42(1-2),71-76(2001)</p>